Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1 to 6 (Canceled).

7. (Currently Amended) A method of data processing using a processor [[,]] comprising [[:]] a reconfigurable field of data processing cells [[;]] and a register, wherein the register has a data stream memory designed to store at least one of a data stream and parts of the data stream, the method comprising:

determining, for the field of data processing cells, configurations corresponding to a program and by running of which the program is executed;

determining, for each configuration, a respective maximum allowed execution runtime prior to lapse of which the respective configuration is uninterruptible; and

providing the program to the processor for execution of the program.

8. (Currently Amended) The method ~~processor~~ as recited in claim 7, further comprising:

using at least one of: i) [[of]] a register allocation device to allocate the register, and ii) a register releasing device to release the register.

9. (Currently Amended) The method ~~processor~~ as recited in claim 8, wherein the register allocation device is ~~configured to be~~ preserved over multiple reconfigurations of the reconfigurable field of data processing cells.

10. (Currently Amended) The method ~~processor~~ as recited in claim 7, wherein the register is a RAM PAE.

11. (Currently Amended) The method ~~processor~~ as recited in claim 7, wherein the program includes a multitask application, the method further comprising: ~~a second~~

using the register: ~~configured~~

to provide read and write access when a virtual FIFO dividing line is implemented; and

for execution of at least one of two different tasks of the multitask application.

12. (Currently Amended) The method ~~processor~~ as recited in claim 7, further comprising:

using at least one memory unit ~~configured for use~~ as a stack and ~~being configured~~ to indicate at least one of a stack underflow state and a stack overflow state.

13. (Currently Amended) The method ~~processor~~ as recited in claim 12, wherein ~~the at least one measuring unit is configured to indicate~~ the at least one of the underflow state and overflow state is of an operating system unit.

14. (New) The method of claim 11, further comprising:

using the register to provide read and write excise when the virtual FIFO dividing line is implemented.